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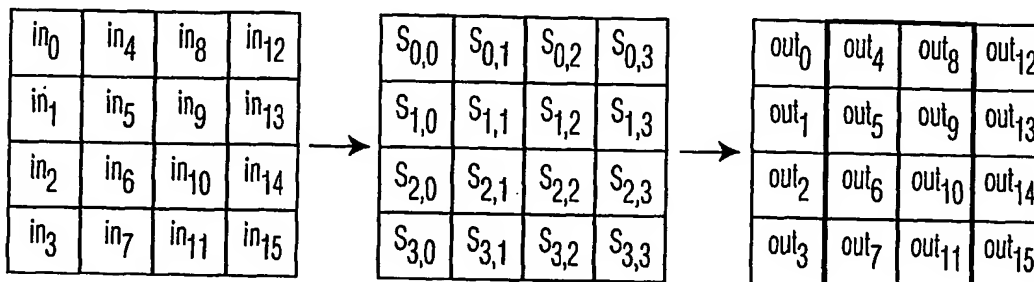
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(54) Title: A SMALL HARDWARE IMPLEMENTATION OF THE SUBBYTE FUNCTION OF RIJNDAEL



(57) Abstract: A small hardware implementation is provided for the Advanced Encryption Standard SubByte function that implements the affine transform and inverse transform in a single Affine-All transform using a multiplicative inverse ROM. The logic is greatly reduced and the maximum path delay is reduced compared to a multiplexor implementation and is slightly greater than a ROM implementation.

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